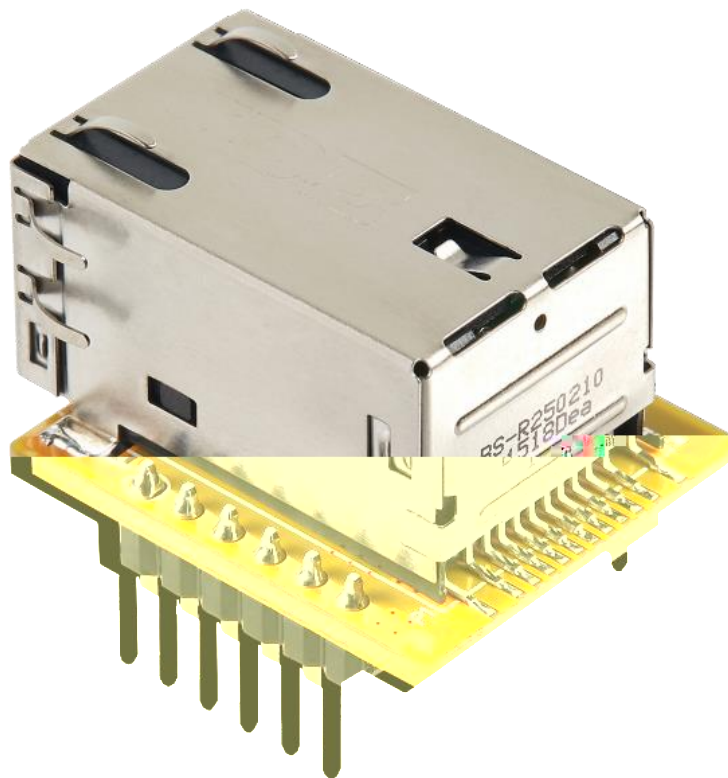


# WIZ850io Datasheet

## (Version V1.0)



## Document Revision History

| 2016-04-28 | V1.0 | Official Release |
|------------|------|------------------|
|            |      |                  |
|            |      |                  |

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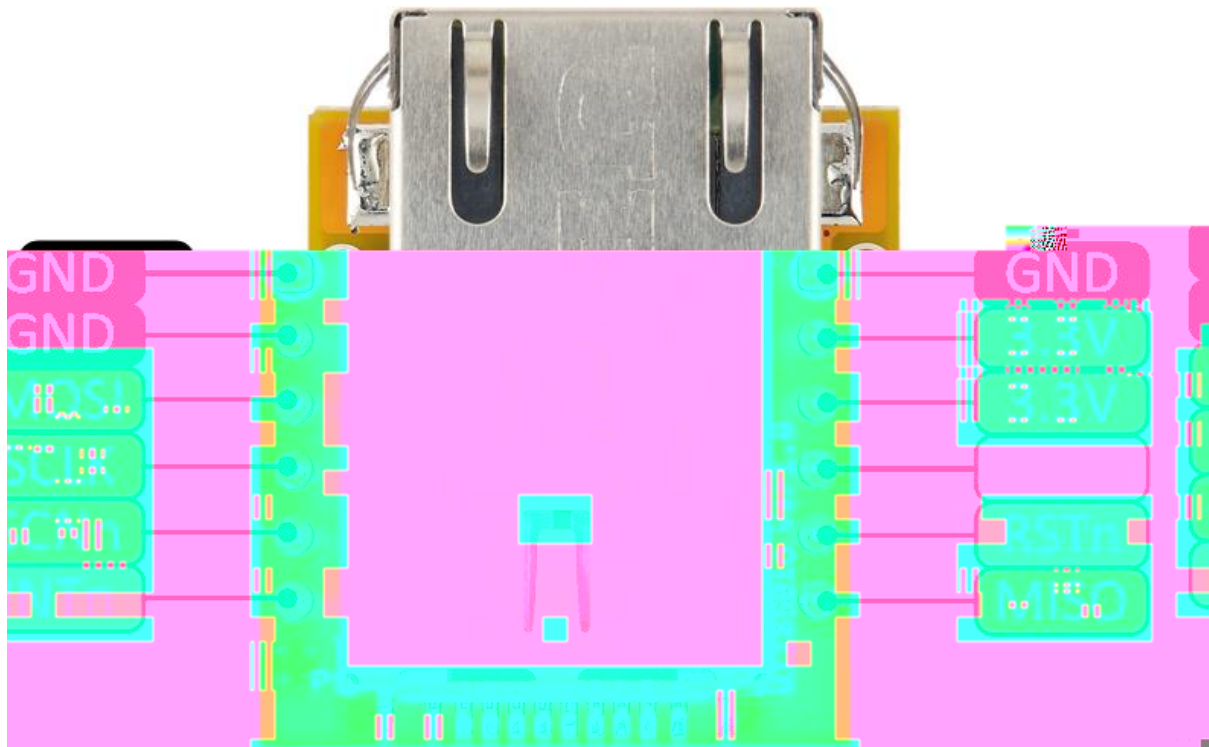
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## 2. Hardware Specification

### 2.1. WIZ850io Hardware Summary

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### 2.2. Pinout



### 2.3. Pin Description

|    | Pin No. | Pin Type | Pin Name | Description             |
|----|---------|----------|----------|-------------------------|
| J1 | 1       | P        | GND      | Ground                  |
|    | 2       | P        | GND      | Ground                  |
|    | 3       | I        | MOSI     | SPI Master Out Slave In |
|    | 4       | I        | SCLK     | SPI Clock               |
|    | 5       | I        | SCNn     | SPI Slave Select        |
|    | 6       | I        | INTn     | W5500 Interrupt         |

|    | Pin No. | Pin Type | Pin Name | Description             |
|----|---------|----------|----------|-------------------------|
| J1 | 1       | P        | GND      | Ground                  |
|    | 2       | P        | 3.3V     | Power                   |
|    | 3       | P        | 3.3V     | Power                   |
|    | 4       | -        | NC       | Not Connect             |
|    | 5       | I        | RSTn     | Reset                   |
|    | 6       | O        | MISO     | SPI Master In Slave Out |

P : Power , I : Input , O : Output

### 3. Characteristic

#### 3.1. DC Characteristic

| Symbol   | Parameter                                 | Pins | Min | Typ | Max | Unit |
|----------|---|------|-----|-----|-----|------|
| $V_{DD}$ | Supply voltage                            |      |     |     |     |      |
| $V_{IL}$ | High level input voltage                  |      |     |     |     |      |
| $V_{IH}$ | Low level input voltage                   |      |     |     |     |      |
| $V_{OL}$ | Low level output voltage                  |      |     |     |     |      |
| $V_{OH}$ | High level output voltage                 |      |     |     |     |      |
| $I_{OL}$ | Low level output Current                  |      |     |     |     |      |
| $I_{OH}$ | High level output Current                 |      |     |     |     |      |
| $I_{DD}$ | Supply Current<br>(Normal operation mode) |      |     |     |     |      |
| $I_{DD}$ | Supply Current<br>(Power Down mode)       |      |     |     |     |      |

#### 3.2. Power Dissipation

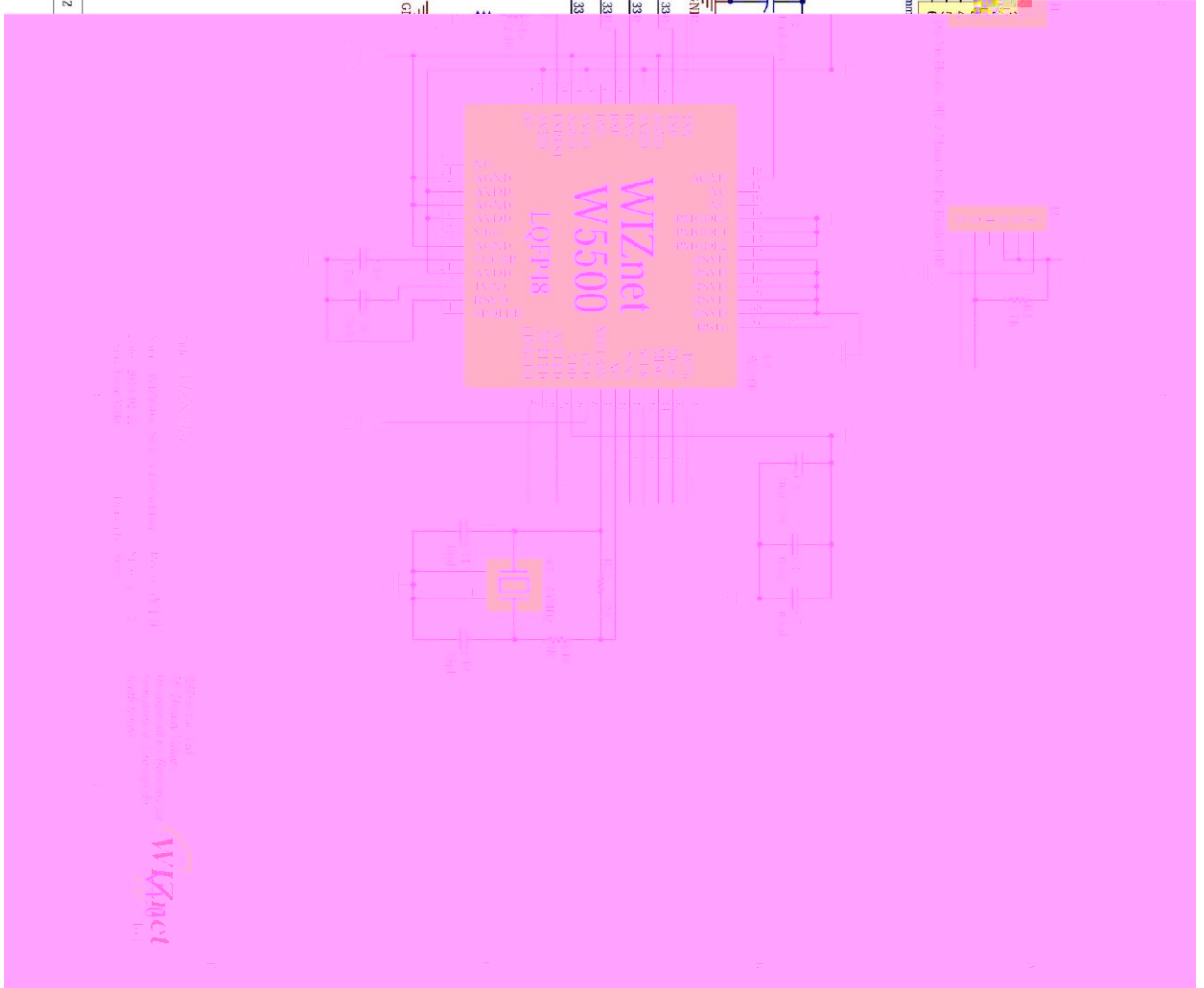
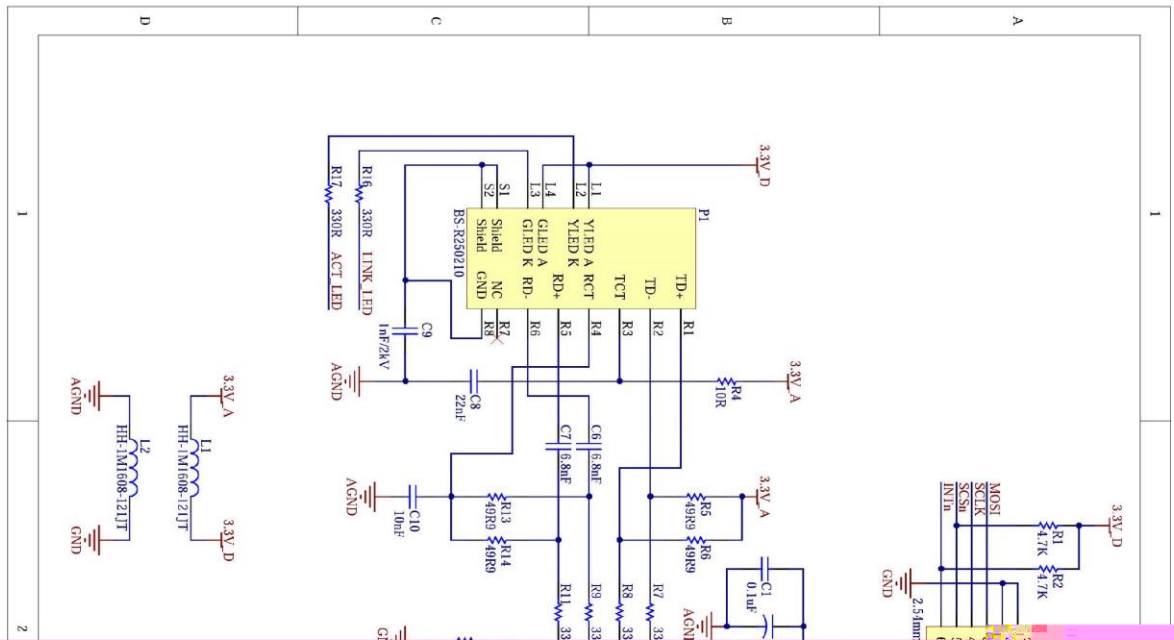
| Condition                           | Min | Typ | Max | Unit |
|-------------------------------------|-----|-----|-----|------|
| 100M Link                           |     |     |     |      |
| 10M Link                            |     |     |     |      |
| Un-Link<br>(Auto- negotiation mode) |     |     |     |      |
| 100M Transmitting                   |     |     |     |      |
| 10M Transmitting                    |     |     |     |      |
| Power Down mode                     |     |     |     |      |

### 4. SPI Operations



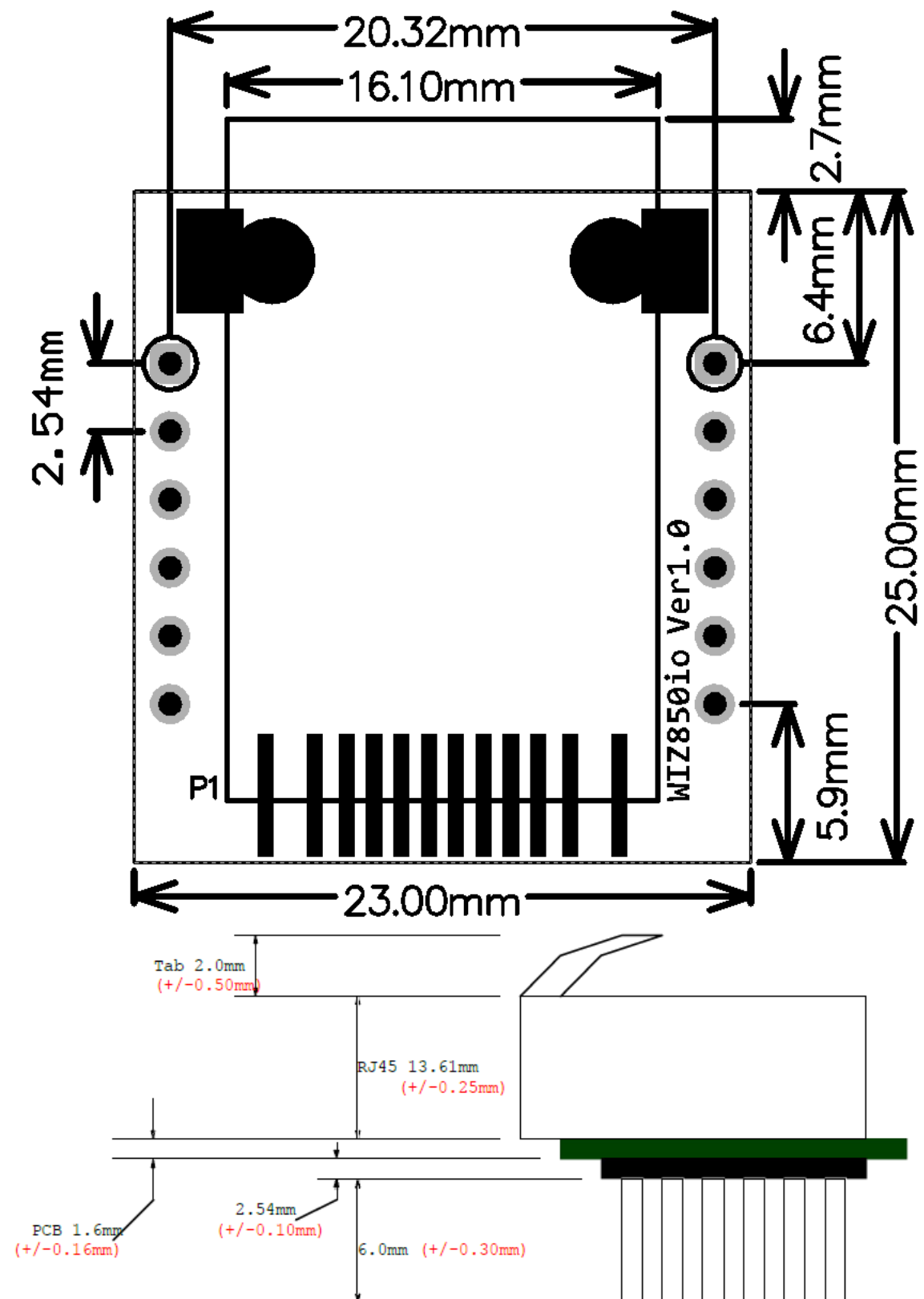


## 6. Schematic



## 7. Dimension

3D PDF Link [3D PDF Link](#)



Total Height: 2mm(Tap) + 13.61mm(RJ45) + 1.6mm(PCB) + 8.54mm(Pin Header) = 25.75mm