New in version 9

- Full Vista and Windows 7 compatibility
- Multi core support
- Enhanced analysis speed and convergence
  Up to 10 times higher speed on 1 core, 15x on dual core, 20x on quad core
- Advanced powerful Spice-VHDL mixed mode analysis
- Extended catalogue with ARM 7, ARM 9 and HCS08 microcontrollers, more SMPS ICs models, realistic ADC and DAC models, LCD display, Bi-colour LEDs
- Extended MCU simulation, USB and other modules
- Programmable Design Tool where users can implement design procedures for calculating and setting circuit parameters so that the circuits can produce predefined target output values.
- Extended flowchart tool: PIC, AVR, 8051, code box, USART, external interrupt handling
- Open and save TINA designs, models and libraries directly from the Web
- Import Spice .CIR and .LIB files directly from the Web
- Import Libraries, Designs and Examples from any earlier versions of TINA v7 and later.
- Show analysis results on diagrams during analysis
- Fast diagram drawing and processing speed, even for very large circuits
- New tool in the View menu of the diagrams for easier hiding and showing curves on diagrams
- New more effective storing of analysis results, save all analysis results for all circuits without speed compromise
- Parametrizable output curve filter for even more effective storage of results
- Scrollbars for browsing multiple diagrams on the same page
- New diagram cursor tools to find and annotate
  local and global maximum, minimum
  gain and phase margins
  zero crossover frequency
  horizontal and vertical coordinates
- Combinational and parallel stepping , show stepping cases (test benches) in separate diagrams
- More advanced tools for addition of parts to TINA from semiconductor manufacturers both from downloaded files and directly from the internet
- Node list in post processor now displays macro names and local node names in the relevant macros
- Minimize TINA main window while analysis is running
- Run two TINA instances on the same computer
- Multi-level, hierarchical ERC. Checks the main or any subcircuts, or the whole circuit in full depth
- ERC detects and shows floating nodes in Spice subcircuits
- Open Examples command in File menu, for easier return to TINA's Examples folder
- Open .CIR files from Open command in the File menu
- New arrow button in Open dialog, for easier finding TINA EXAMPLES or User folders
- Updated and extended examples under the EXAMPLES folder
- Conversion of circuits to TINA v7 format, for compatibility with TINA v7 and v8 and v9
- Detailed Transient Statistics from the View menu
- TINA v9 is now digitally signed

PCB Design

- G-Code export of PCB designs for creating control files for milling machines
- Extended PCB footprint catalogue

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