Surname:	Branch					Forenames:		Q						
Learner Registration Number:	Q	В	0	0	7	0	0	Centre Number:	7	6	5	4	3	

## LOG BOOK RECORD: SESSION #1

### **General Comments:**

Activity 1 - Task planning and system design changes – see TASK\_PLAN\_76543\_QB00700\_BRANCH\_Q.pdf .

Activity 2 - Analysis of brief

Must have	Good to have	Nice to have
		Identifies Tooley-!ober "otels# as the o\$ner of the device.
	When the c)stomer does not have a preference* this saves time in a b)sy kitchen environment +see iss)es belo\$,.	
/ andatory re0) irement from the client brief +see iss) es belo\$,.		
2is)al indication of cooking# and done#	+!o)d, a)dio indication of done#	
/ andatory re0)irement from the client brief. 2is)al indication.		Additional display – e.g. a bright flashing ! 34 indicates 5 ooking#
/ andatory re0)irement from the client brief.		! 3 4 described above flashes faster for the last '. seconds of cooking
/ andatory re0)irement from the client brief. 2is)al indication.		! 34 described above is %16& contin)o)sly \$hen egg is 4 one#
/ andatory re0)irement from the client brief. 2is)al indication.	A)dio indication of 4 one#.	! 34 described above remains %1 6& and b)77er so)nds )ntil %5ancel& key is pressed.
	/ andatory re0)irement from the client brief +see iss)es belo\$,.  2is)al indication of cooking# and done#  / andatory re0)irement from the client brief. 2is)al indication.  / andatory re0)irement from the client brief.  / andatory re0)irement from the client brief.	When the c)stomer does not have a preference* this saves time in a b)sy kitchen environment +see iss)es belo\$,.  / andatory re0)irement from the client brief +see iss)es belo\$,.  2is)al indication of cooking# and done#  / andatory re0)irement from the client brief. 2is)al indication.  / andatory re0)irement from the client brief.  A)dio indication of 4 one#.

1 vercooked indication			! 3 4 described above flashing pattern indicates that the egg is overcooked if the set cooking period has finished and device has not been attended to +see iss)es belo\$,.
5ancel Timing	/ andatory re0) irement from the client		
Test / ode		8or testing* an option to r)n the egg timing at a higher rate e.g. 91. faster \$0)ld speed )p testing time considerably.	

# **Initial Test Plan:**

Test #	Purpose of test	Test Data	Expected Result	Actual Result	Comments and justification
1	Test \$elcome# message	:o\$er on 4evice	\$elcome# message displayed		
2	Test one key %standard& egg f)nction	: ress key for a %standard& ' ( min)te egg	4evice indicates 5ooking# and starts do\$n co)nting from ';'. to .; When .; reached* device indicates 4 one# )ntil 5ancel# pressed by )ser.		
,	Test o)t of range timings	Try to program a cooking time belo\$ 2 min)tes and above - min)tes	The device sho)ld not allo\$ cooking times less than 2 min)tes or more than - min)tes.		
<	Test the %cancel& cooking f)nction	Select 2 min)tes cooking time and press start#. : ress cancel# after a fe\$ seconds	4 evice indicates 5 ooking# and starts do\$n co)nting. When the 5 ancel# key is pressed* the timer stops and displays 5 anceled#		

=	Test invalid key press \$hen r)nning	Select 2 min)tes cooking time and press start#. 1 nce r)nning* press other keys* e9cept cancel#	4 evice indicates 5 ooking# and starts do\$n co)nting. All key presses sho)ld be ignored. When the cancel# key is pressed* the timer stops and displays 5 ancelled#		
-	Test fast timing mode	3nable the fast timing mode on the device	>epeat tests 2 in fast timing mode. The only difference sho)ld be the cooking time is red)ced.		
?	Test a - min)te egg in fast timing mode	Select - min)tes cooking time and press start#.	4 evice indicates 5 ooking# and starts do\$n co)nting from -; to .; When .; reached* device indicates 4 one# )ntil 5 ancel# pressed by )ser.		

### Issues encountered and solutions with justification:

- +1, Where a c)stomer does not specify the time to boil their egg\* I ass)me a standard egg takes ' (min)tes to boil. This time needs approval by the head chef before the )nit is mass prod)ced.
- +2, 3ven tho)gh the client brief s)ggests that only = boiling times are re0)ired +2\*'\*<\*= and min)tes,\* I think that a finer ad@)stment is needed and the time needs to be ad@)stable in increments of 1. seconds as there is too m)ch difference bet\$een a 2 min)te egg and a ' min)te egg +i.e. = . A e9tra time,.
- +', The device \$ill be \$orking in a noisy environment\* so vis)al indication is more important than a)dio indication\* )nless the a)dio indication is lo)d.
- +<, When the boiling time has elapsed\* there is a possibility that the egg \$ill be overcooked if not attended to promptly. As a nice to have# feat)re\* I have considered )sing is an e9tra timer to check for overcooking.

#### Action list for the next session:

8rom my operational re0) irements#;

- +1, / ake a list of possible inp)t and o)tp)t hard\$are options and microcontrollers that co)ld achieve my sol)tion.
- +2, / ake a table noting the advantages and disadvantages of each element.
- +', / ake my selection\* giving reasons for my choice
- +<, 5 reate an o)tline plan for my system that meets the client brief
- +=, Start my hard \$ are design circ) it +schematic, diagrams\* interconnection lists\* pin f)nctions etc.
- +-, Start my soft\$are design 4ecision tables\* 8lo\$charts etc.

  / y first soft\$are design sho)ld test most of the hard\$are f)nctionality\* \$ith hooksBst)bs to add f)nctionality as the task progresses.